

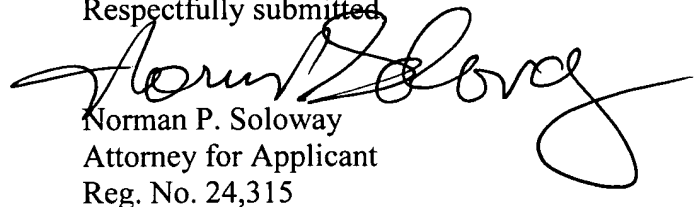
REMARKS

The rejection of claims 1-8 under 35 USC § 102 as being anticipated by Applicants' admitted prior art is in error. Independent claims 1 and 5 require that "the load current supplying circuit being controlled by voltages at said third and forth nodes". Nowhere does Applicants' admitted prior art teach this feature. As is clearly shown in prior art Figure 2, the third and fourth nodes correspond to N_1' and N_2' and are not connected to the duty ratio to the detecting circuit in such a way that they could control the load current supplying circuits. Thus, Applicants' admitted prior art does not teach claims 1 and 5, and the rejection of claims 1 and 5 and claims 2-4 and 6-8 which depend thereon is in error.

Having dealt with all the objections raised by the Examiner, the Application is believed to be in order for allowance. Early and favorable action are respectfully requested.

In the event there are any fee deficiencies or additional fees are payable, please charge them (or credit any overpayment) to our Deposit Account Number 08-1391.

Respectfully submitted,



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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: MAIL STOP AMENDMENT, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

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By



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